

Resume of C. McCord



<http://www.adapticom1.net/cmccord/mccord-Mgr-resume2016-03-22.html>

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P.O. Box 91462, Raleigh, NC 27675
Revision "C", last modified 03/22/2016

Summary:

Senior IT / Engineering Manager with management experience since 2001. Extensive technical background as an electronic / computer engineer. Twenty five years in the computer / electronics field, eleven years as an electronic engineer, seven years in project management, and ten years as an engineering manager. Twenty year history with projects at IBM (T.J. Watson Research, Boca Raton, RTP, Guadalajara). Currently managing an IT department comprised of six other managers and associates. Recent experience managing short-term, fast-paced projects as well as long term, multi-year, endeavors (tracking, projections, Gantt charts, etc. done through "Microsoft Project" and other custom tools). Experienced with managing and participating in projects spread over wide geographic areas and time zones. Comfortable with HTML / Web development, Microsoft Office Suite, administration of domains, etc. specializing in managing product development teams and projects.

Current practitioner of "Lean" Continuous Improvement methodology, using DMAIC, Kaizen and 5S improvement techniques, along with Value Stream mapping and analytics to assist project leads in process improvement initiatives, and the identification of non-value added activities. Recent graduate of Project Management Academy Boot Camp for PMP certification.

Both management and hands-on skills for development projects utilizing microcontrollers, integrated circuits, discrete components, & FPGAs. Successful designs using Microchip, Renesas, & Motorola microcontrollers, along with Actel, Altera, & Xilinx FPGAs and FPSoCs. Focus on industrial automation; development of commercial products, utilizing Mentor DxDesigner (Viewdraw), Orcad, & Allegro from both Unix and Windows based platforms. Designs for wireless applications utilizing various GSM, CDMA, & ISM chipsets and modules for cellular data communications. Expertise in personnel and project management as well as product certification (Part 15, Part 68, etc.) & EMI mitigation. Experienced in the use of logic analyzers and sophisticated test equipment along with development of custom tools, including data collection hardware. Software / admin experience with "C"/"C++" and Perl in Linux/Unix/AIX and Windows environments. Licensed Amateur Radio Operator (KJ4UGB). BSA Scout leader and avid runner (half-marathons).

Employment:

March 2011 to Present

Wright Global Graphics, 5115 Prospect St., Thomasville, NC 27360

IT Manager

Managing department of six utilizing "Lean"CI methodology, and member of corporate steering committee.

- Ruby on Rails & Spree project for e-commerce site
- iOS & Android 3-D phone application for Restonic Corporation
- Augmented reality projects focusing on 3-D displays in Android and iOS phone applications
- Benchmarking project comparing Red Hat vs. Microsoft VDI solutions for small enterprises
- Implementation of small scale corporate VDI solution
- Migration from iOS based server storage to NAS
- Corporate migration from Exchange 2003 to Exchange 2013 online
- Management of server farm comprised of both Windows and Linux
- Management of corporate workstation environment comprised of 60% windows, 40% Mac
- Corporate migration from antiquated Vodavi phone system to Zultys VoIP solution
- Management and utilization of MAS 500 ERP, and Quickbooks environments.

June. 2007 to Present (Concurrent with above)

Adapticom Inc., P.O. Box 91461 Raleigh, NC 27675-1461 919-870-0608

Engineering Manager/Principal Engineer:

- Management of Product Development company, with seventeen member team, taking new designs from concept to production prototypes,

focusing on firmware and application software, completing more than thirty designs in a three year period.

- iOS development of Social Media marketing & sales application
- Designed, fabricated & project managed a multi-mode communications device transmitting GPS data via GPS & CDMA cellular networks, as well as satellite, and other proprietary channels.
- Designed, fabricated & project managed a multi-mode communications device transmitting various types of telemetry data.
- Utilized DxDesigner(Viewdraw), OrCAD, and other tools to create block diagrams and schematics of a wide variety of electronic designs.
- Managed personnel and contract resources for up to five simultaneous projects, utilizing "Microsoft Project" and other performance tracking tools.
- Participated in and managed team providing round-the-clock VHDL development for expedited FPGA project.
- Directed small production facility performing test, assembly, packaging, and shipping of products on a weekly basis.
- Simultaneously acted as engineering director for one start-up and two established North Carolina engineering firms.

Oct. 2003 to May 2007

Adapticom Inc., P.O. Box 91461 Raleigh, NC 27675-1461 919-870-0608

Engineering Manager/Principal Engineer:

- Electro-mechanical development of a family of microprocessor based inflators for the bedding industry.
- Designed IP Video Surveillance system for small business owners needing to actively monitor multiple sites in real-time for loss prevention and security. Completed roll-out into fast food restaurant chain.
- Provided time-critical rework and configuration services for large computer manufacturers.
- Provided impartial testing services for evaluating consumer appliances.
- Completed FPGA Verilog based reference designs for Actel ProASIC, Altera Max 3000, and Xilinx Spartan II, using Verilog, Viewlogic, Modelsim, and associated development tools.
- Design development based on 56800E family of hybrid microcontrollers, utilizing CodeWarrior and Processor Expert .
- FPGA design focusing on the mitigation of security vulnerabilities using Actel, Altera, and Xilinx reconfigurable FPGAs and FPSoC, utilizing Verilog, Modelsim, and associated development tools.

Mar. 2003 to Oct. 2003

Adapticom Inc., P.O. Box 91461 Raleigh, NC 27675-1461 919-870-0608

Engineering Manager/Principal Engineer; management of Adapticom Engineering Team. Conducted analysis of Actel, Altera & Xilinx FPGA security vulnerabilities (bitstream security & encryption, configuration corruption, data separation, TMR, microprobing, Focused ION Beam workstations, Electron Beam Testers, decapping, power analysis, Lithium Niobate, data remanence, SEU & SEL, etc.). Furnished clients with full range of **new product development** expertise.

Jun. 2001 to Feb. 2003

Adapticom Inc., P.O. Box 91461 Raleigh, NC 27675-1461 919-870-0608

Engineering Manager/Principal Engineer; management of Adapticom Engineering Team. VoIP designs utilizing a wide variety of components and technologies, including the Atmel AT75C310 dual DSP processor and Motorola MC68EN302 Integrated Multiprotocol Processor accompanied by the CT8020 TrueSpeech co-processor, along with Si3044 Silicon Labs DAA (Data Access Arrangement) line interface, OKI MSM7716 "Single Rail" linear CODEC , National TP3054 CODEC, SiLabs Si3220 Dual ProSLIC, STMicroelectronics STLC3055 SLIC, Legerity **Am79R70** SLIC, Intersil HC55181 SLIC, discrete SLICs, as well as Atmel Fast-VirtualNet, Atheros Communication AR5001X Combo WLAN solution, AMD Am1772™ wireless chipset, Intel OC192 Wan Uplink, and other circuitry associated with networking, VoIP and wireless communications.

Furnished Net2Phone Inc. with new product development expertise including: initial electrical design, raw PCB layout and fabrication, prototype builds, mechanical design, Bill of Materials (BOM) costing / cost reduction, and certifications (Part 15, Part 68, etc.) - mitigation work. Responsible for all hardware engineering involved in bringing to market Net2Phone's recent product, the Yap Jack Plus .

Nov. 1999 to Jan. 2001

Net2Phone Inc., Hackensack, NJ

Consulting Engineer; duties included: Investigation of IP telephony possibilities over cable and telephone systems, attendance at trade shows and evaluation of telephony products, and interfacing with Net2Phone business partners to develop plans for fast paced cable telephony projects and trials. Managed engineering team providing documentation and explanations of telephony standards and protocols, along with general industry references.

Feb. 1995 to Jan. 2000

**IBM Corp., Dept. 2S9, Server Performance Analysis,
Research Triangle Park, NC.**

Contract Engineer; duties included: Sub-system, module, and gate level design of realtime high speed digital systems utilizing Actel and Xilinx Field Programmable Gate Arrays , Verilog, Synopsys, ModelSim, Synplicity, and Viewlogic schematic capture. Conducted Verilog/VHDL benchmarking on Actel & Xilinx FPGAs. System level design of realtime high speed digital systems capable of collecting data at 1600 mbytes/second.

[HTML resume, next page](#)

